

FIG. 1

PEARCE 26; 09/755,826
METHOD OF MANUFACTURING A laterally
diffused metal oxide semiconductor device
Hitt Gaines, PC; Greg H. Parker (972)480-8800

REPLACEMENT
SHEET

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FIG. 2

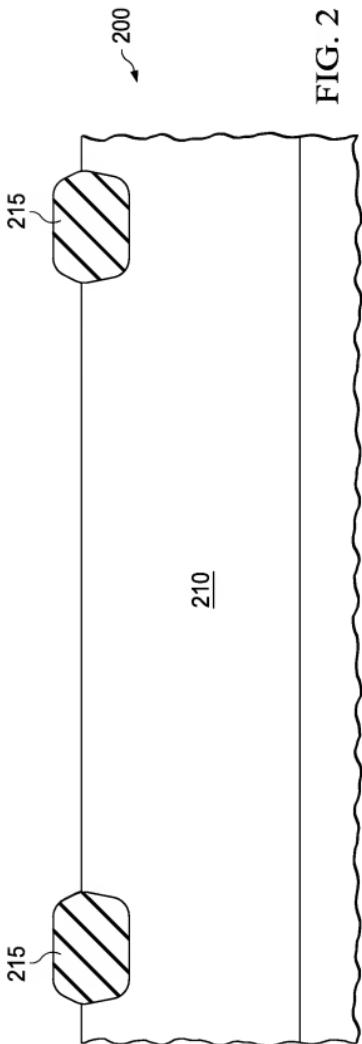
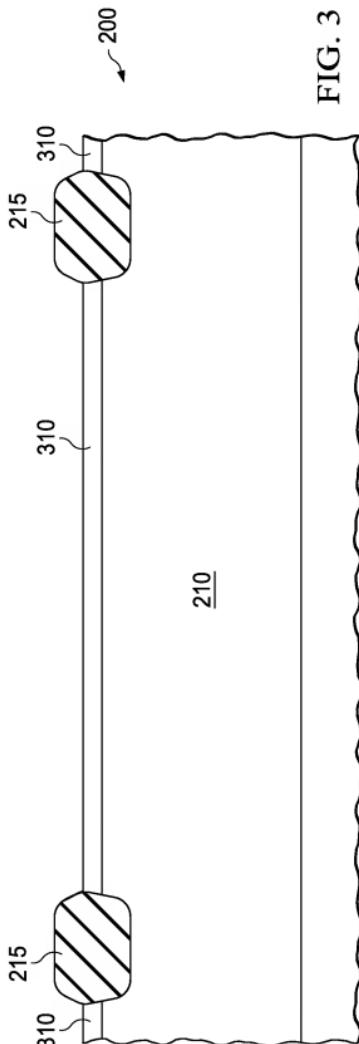


FIG. 3



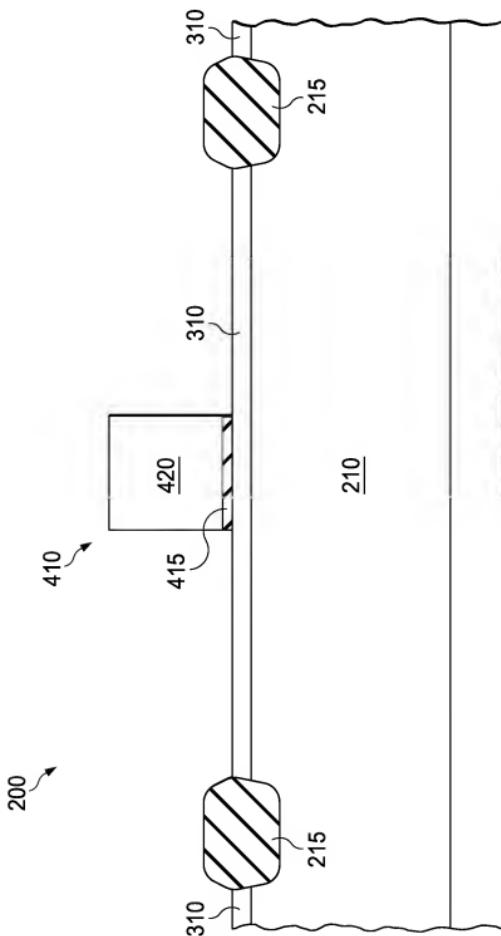


FIG. 4

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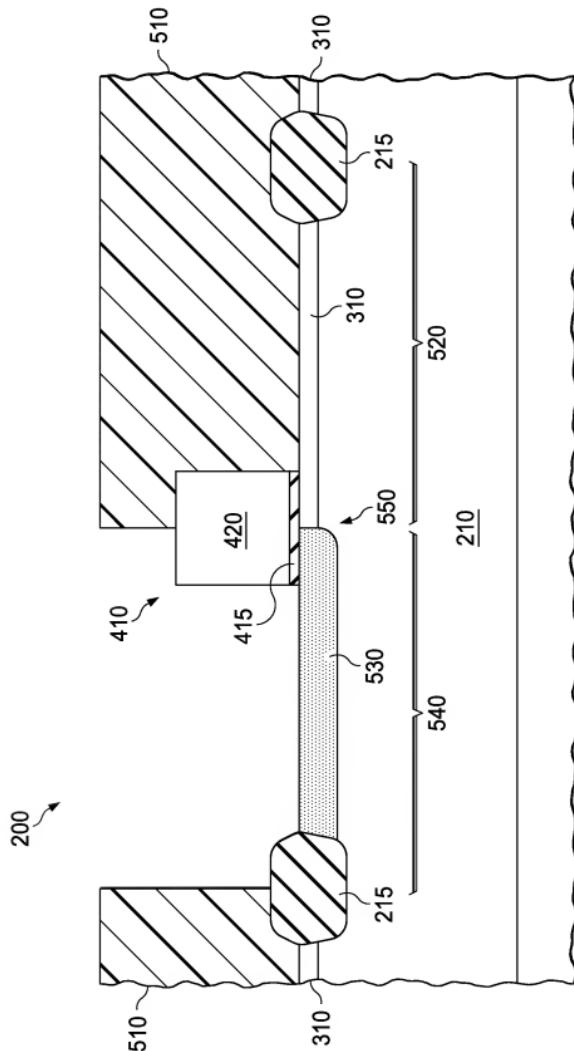


FIG. 5

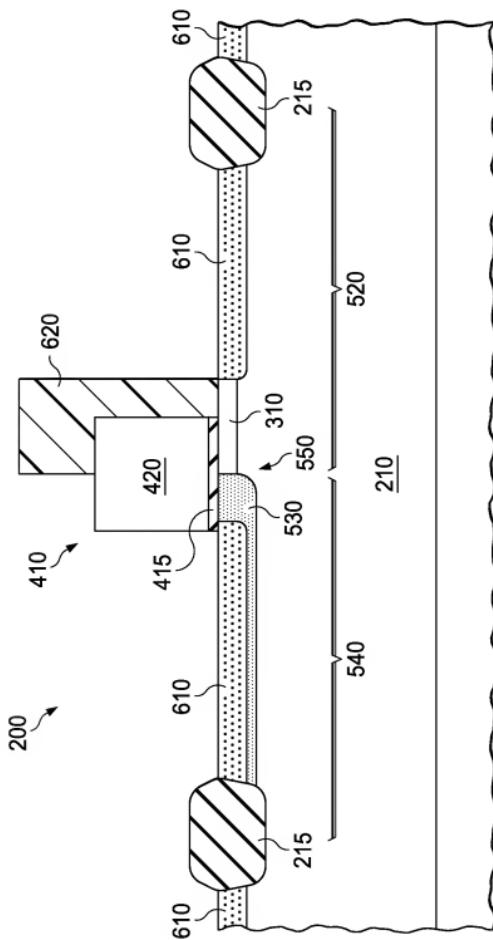


FIG. 6

